

**S/N 10/810,035**

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant:	Albert S. Weiner	Examiner:	Thinh Nguyen
Serial No.:	10/810,035	Group Art Unit:	2818
Filed:	March 26, 2004	Docket No.:	2800.450US1
Customer No.:	76287	Confirmation No.:	8458
Title:	Non-volatile transistor memory array incorporating read-only elements with single mask set		

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**RESPONSE TO EX PARTE QUAYLE ACTION**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

In response to the Ex Parte Quayle Action dated January 4, 2010, Applicant requests reconsideration of the above-identified application in view of the following.